



- Synchronous counting and loading
- Two count enable inputs for n-bit cascading
- Positive-edge triggered clock
- Synchronous reset

#### DESCRIÇÃO

Synchronous presettable binary counters which feature an internal look-ahead carry and can be used for high-speed counting. Synchronous operation is provided by

#### CARACTERÍSTICAS

Aplicação	High-speed counting
Família	CMOS
	<ul style="list-style-type: none"> <li>• Min:4.5V</li> <li>• Typ:5V</li> <li>• Max:5.5V</li> </ul>
Tensão DC	

Caixa	DIP-16
Tipo de embalagem	Bulk
Quantidade por Embalagem	1

